

AUTOMOTIVE CURRENT TRANSDUCER OPEN LOOP TECHNOLOGY HAH3DR 800-S07/SP1





Introduction

The HAH3DR-S07 family is a tri-phase transducer for DC, AC, or pulsed currents measurement in automotive applications. It offers a galvanic separation between the primary circuit (high power) and the secondary circuit (electronic circuit).

Features

- Open Loop transducer using the Hall effect sensor
- Low voltage application
- Unipolar +5 V DC power supply
- Primary current measuring range up to ±800 A
- Maximum RMS primary admissible current: limited by the busbar, the magnetic core or ASIC T < +125 °C
- Operating temperature range: -40 °C < T < +125 °C
- Output voltage fully ratiometric (in sensitivity and offset).

Special features

- All in one tri-phase transducer
- Perfect fit to 'HybridPACK TM' drive Infineon
- Simplified installation with press fit contacts eliminates soldering
- Built-in nuts for busbar attachement.

Advantages

- Excellent accuracy
- Very good linearity
- Very low thermal offset drift
- Very low thermal sensitivity drift
- Wide frequency bandwith
- No insertion losses
- Very fast response time.

Automotive applications

- Starter Generators
- Inverters
- HEV applications
- EV applications
- DC / DC converter.

Principle of HAH3DR S07 family

The open loop transducers uses a Hall effect integrated circuit. The magnetic flux density B, contributing to the rise of the Hall voltage, is generated by the primary current IP to be measured. The current to be measured IP is supplied by a current source i.e. battery or generator (Figure 1). Within the linear region of the hysteresis cycle, B is proportional to:

$$B(I_{\rm P}) = a \times I_{\rm P}$$

The Hall voltage is thus expressed by:

$$V_{\rm H} = (c_{\rm H}/d) \times I_{\rm H} \times a \times I_{\rm P}$$

Except for $I_{\rm p}$, all terms of this equation are constant. Therefore:

| $V_{\rm H} = b$ | $P \times I_P$ |
|----------------------|--------------------------------|
| a | constant |
| b | constant |
| С _н | Hall coefficient |
| $d^{c_{_{_{_{H}}}}}$ | thickness of the Hall plate |
| I_{μ} | current across the Hall plates |

The measurement signal $V_{\rm H}$ amplified to supply the user output voltage or current.

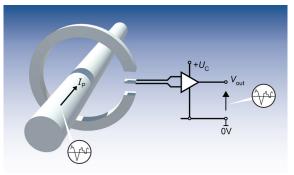
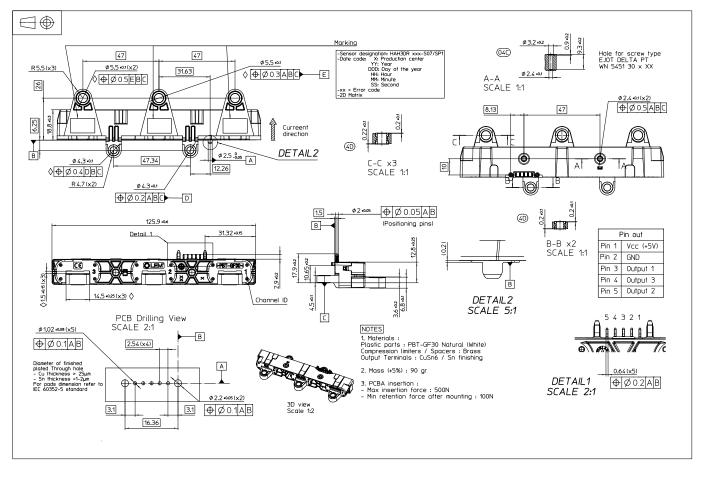


Fig. 1: Principle of the open loop transducer.



Dimensions (in mm)



Mechanical characteristics

- Materials
- Magnetic core
- Pins
- Mass
- IP level

Mounting recommendation

- See dimensions
- The clamping force must be applied to the compression limiter, washer recommended.

See dimensions

FeSi wound core

See dimensions

90 g ±5 %

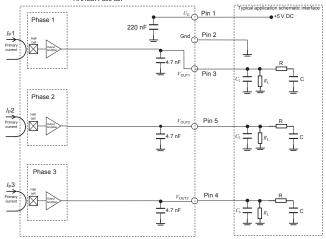
IPxx.

Secondary connection Pressfit

Remark

 $V_{\rm out}$ > $V_{\rm o}$ when $I_{\rm p}$ flows in the positive direction (see arrow on drawing).

System architecture (example)



 C_{L} < 2.2 nF EMC protection (optional) RC Low pass filter (optional)

On board diagnostic

 $R_{\rm I}$ > 10 k Ω . Resistor for signal line diagnostic (optional)



Absolute ratings (not operating)

| Parameter | Symbol | Unit | Specification | | | Conditions |
|------------------------------------|-----------------|------|---------------|---------|-----|--|
| Falanielei | | | Min | Typical | Max | Conditions |
| | | | | | 8 | Continuous not operating |
| Maximun supply voltage | U _c | V | -0.5 | | 6.5 | Exceeding this voltage may temporarily reconfigure the circuit until the next power on |
| Ambient storage temperature | Ts | °C | -40 | | 125 | |
| Electrostatic discharge voltage | $U_{\rm ESD}$ | kV | | | 8 | IEC 61000-4-2 |
| RMS voltage for AC insulation test | $U_{\rm d}$ | kV | | | 2.5 | 50 Hz, 1 min, IEC 60664 part1 |
| Creepage distance | d _{Cp} | mm | | 5.2 | | |
| Clearance | d _{CI} | mm | | 4.6 | | |
| Comparative traking index | CTI | | | PLC3 | | |
| Insulation resistance | R _{IS} | MΩ | 500 | | | 500 V DC, ISO 16750 |
| Primary current | I _P | А | | | | Current limited by busbar temperature < 125°C |

Operating characteristics

All characteristics noted under conditions –800 A $\leq I_{p} \leq$ 800 A, 4.75 V $\leq U_{C} \leq$ 5.25 V, –40 °C $\leq T_{A} \leq$ 125 °C, unless otherwise noted.

| Dorromotor | 0 | 11 14 | Specification | | | | |
|--|--------------------|---------|--------------------|------------------------------------|--------------------|--|--|
| Parameter | Symbol | Unit | Min | Typical | Max | Conditions | |
| Electrical Data | | | | | | | |
| Primary current, measuring range | I _{PM} | A | -800 | | 800 | | |
| Supply voltage ¹⁾ | Uc | V | 4.75 | 5 | 5.25 | | |
| Ambient operating temperature | T _A | °C | -40 | | 125 | | |
| Output voltage (Analog) | $V_{\rm out}$ | V | $V_{\rm out}$ = (U | (V ₀ - V ₀ - | $G \times I_{P}$) | @ T _A = 25 °c | |
| Sensitivity | G | mV/A | | 2.5 | | | |
| Offset voltage | Vo | V | | 2.5 | | | |
| Current consumption | I _c | mA | | 45 | 60 | @ U _c = 5 V | |
| Load resistance | R | kΩ | 10 | | | | |
| Output internal resistance | R _{out} | Ω | | 1 | 10 | | |
| | | Perform | nance Dat | а | | | |
| Ratiometricity error | € _r | % | | ±0.5 | | | |
| Sensitivity error | ε _g | % | | ±0.6 | | @ T _A = 25 °C, @ U _C = 5 V | |
| Electrical offset voltage | V _{oe} | mV | | ±4 | | @ $T_{\rm A}$ = 25 °C, @ $U_{\rm C}$ = 5 V | |
| Magnetic offset voltage | V _{om} | mV | | ±3 | | @ $T_{\rm A}$ = 25 °C, @ $U_{\rm C}$ = 5 V, after ± $I_{\rm PM}$ | |
| Average temperature coefficient of V_{OE} | TCV | mV/°C | | ±0.05 | | | |
| Average temperature coefficient of G | TCG _{AV} | %/°C | | ±0.03 | | | |
| Linearity error | εL | % | -1 | | 1 | % of full scale | |
| Step response time to 90 % I _{PN} | t _r | μs | | 2 | 6 | $di/dt = 100 \text{ A}/\mu\text{s}$ | |
| Frequency bandwidth ²⁾ | BW | kHz | 40 | | | @ -3 dB | |
| Peak-to-peak noise voltage | V _{no pp} | mV | | | 20 | @ DC to 1 MHz | |
| Start up time | t _{start} | μs | | | 800 | | |
| Phase shift | Δφ | 0 | -4 | | | @ DC to 1 kHz | |

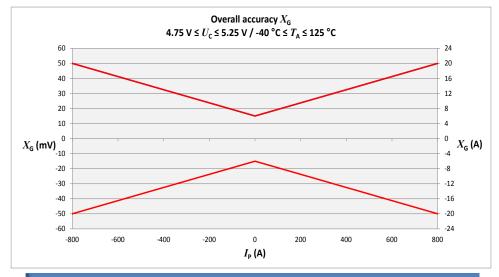
<u>Notes</u>: ¹⁾ The output voltage V_{out} is fully ratiometric. The offset and sensitivity are dependent on the supply voltage U_{c} relative to the following formula:

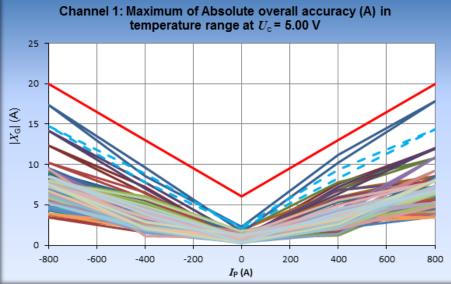
$$I_{\rm P} = \left(\frac{5}{U_{\rm C}} \times V_{\rm out} - V_{\rm O}\right) \times \frac{1}{G}$$
 with G in (V/A)

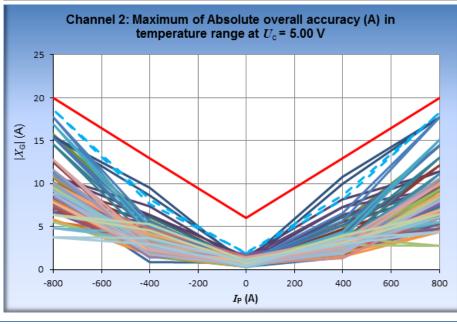
²⁾ Primary current frequencies must be limited in order to avoid excessive heating of the busbar, magnetic core and the ASIC (see feature paragraph in page 1).



Accuracy



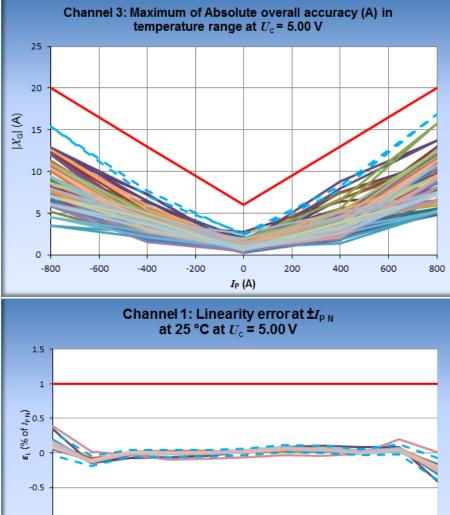


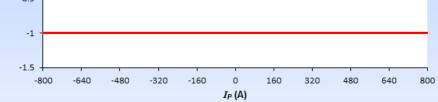


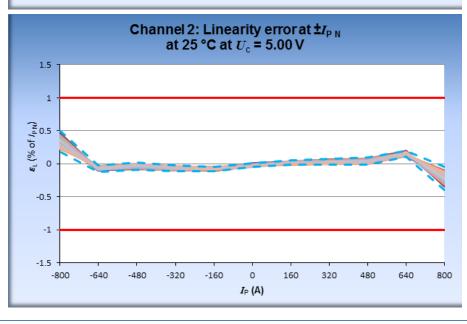
LEM reserves the right to carry out modifications on its transducers, in order to improve them, without prior notice



Accuracy





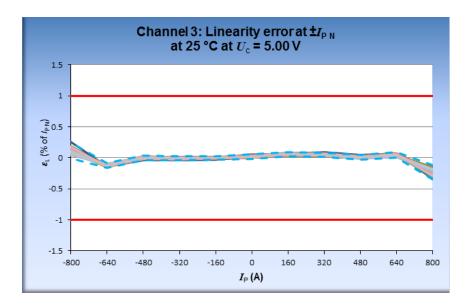


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Accuracy

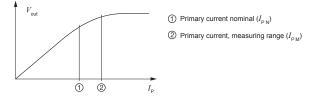


Curve extract from PV test. Dotted line represents the 3σ limit



PERFORMANCES PARAMETERS DEFINITIONS

Primary current definition:



Definition of typical, minimum and maximum values:

Minimum and maximum values for specified limiting and safety conditions have to be understood as such as values shown in "typical" graphs. On the other hand, measured values are part of a statistical distribution that can be specified by an interval with upper and lower limits and a probability for measured values to lie within this interval. Unless otherwise stated (e.g. "100 % tested"), the LEM definition for such intervals designated with "min" and "max" is that the probability for values of samples to lie in this interval is 99.73 %. For a normal (Gaussian) distribution, this corresponds to an interval between -3 sigma and +3 sigma. If "typical" values are not obviously mean or average values, those values are defined to delimit intervals with a probability of 68.27 %, corresponding to an interval between -sigma and +sigma for a normal distribution. Typical, minimum and maximum values are determined during the initial characterization of a product.

Output noise voltage:

The output voltage noise is the result of the noise floor of the Hall elements and the linear amplifier.

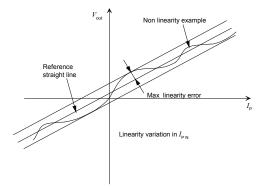
Magnetic offset:

The magnetic offset is the consequence of an any current on the primary side. It's defined after a stated excursion of primary current.

Linearity:

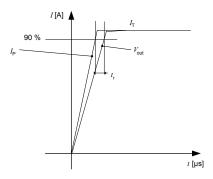
The maximum positive or negative discrepancy with a reference

straight line $V_{out} = f(I_p)$. Unit: linearity (%) expressed with full scale of I_{PN} .



Response time (delay time) *t*_{*i*}:

The time between the primary current signal $(I_{\rm P~N})$ and the output signal reach at 90 % of its final value.



Sensitivity:

The transducer's sensitivity G is the slope of the straight line

 $V_{out} = f(I_{P})$, it must establish the relation:

$$V_{\text{out}}(I_{\text{P}}) = U_{\text{C}}/5 (G \times I_{\text{P}} + V_{\text{O}})$$

Offset with temperature:

The error of the offset in the operating temperature is the variation of the offset in the temperature considered with the initial offset at 25 $^{\circ}$ C.

The offset variation I_{OT} is a maximum variation the offset in the temperature range:

$$I_{OT} = I_{OE} \max - I_{OE} \min$$

The offset drift TCI_{OEAV} is the I_{OT} value divided by the temperature range.

Sensitivity with temperature:

The error of the sensitivity in the operating temperature is the relative variation of sensitivity with the temperature considered with the initial offset at 25 $^{\circ}$ C.

The sensitivity variation G_{τ} is the maximum variation (in ppm or %) of the sensitivity in the temperature range: G_{τ} = (Sensitivity max – Sensitivity min) / Sensitivity at 25 °C. The sensitivity drift TCG_{AV} is the G_{τ} value divided by the temperature range. Deeper and detailed info available is our LEM technical sales offices (www.lem.com).

Offset voltage @ $I_p = 0 A$:

The offset voltage is the output voltage when the primary current is zero. The ideal value of $V_{\rm o}$ is $U_{\rm c}/2$. So, the difference of $V_{\rm o} - U_{\rm c}/2$ is called the total offset voltage error. This offset error can be attributed to the electrical offset (due to the resolution of the ASIC quiescent voltage trimming), the magnetic offset, the thermal drift and the thermal hysteresis. Deeper and detailed info available is our LEM technical sales offices (www.lem. com).

Environmental test specifications:

Refer to LEM GROUP test plan laboratory CO.11.11.515.0 with "Tracking_Test Plan_Auto" sheet.



| Test | Test Standards | | | | | |
|--|-------------------------------|--|--|--|--|--|
| INITIAL CHARACTERZATION | | | | | | |
| Linearity error at 25 °C | LEM CO.60.09.014.0 | | | | | |
| Characterization in temperature range | LEM CO.60.09.014.0 | | | | | |
| LEG 1 : ELECTRICAL PERFORMANCES | | | | | | |
| LEG 1: Frequency bandwidth | LEM 98.20.00.538.0 | | | | | |
| LEG 1 : Output noise (peak-peak) | LEM 98.20.00.575.0 | | | | | |
| LEG 1 : Response time - d <i>i</i> /d <i>t</i> | LEM 98.20.00.575.0 | | | | | |
| LEG 1: dv/dt | LEM 98.20.00.545.0 | | | | | |
| ENVIRONMENTAL TESTS (Climatic) | | | | | | |
| LEG 2 : Thermal shocks | IEC 60068-2-14 | | | | | |
| LEG 3 : High temperature storage | IEC 60068-2-2 | | | | | |
| LEG 4 : Low temperature storage | ISO 16750-4 § 5.1.1.1 | | | | | |
| LEG 5 : Powered temperature cycle | ISO 16750-4 § 5.3.1 | | | | | |
| LEG 1 : Ageing with 85 °C; 85 % RH | CETP: 00.00-E412 § 5.17 | | | | | |
| LEG 6 : Sine vibration test | ISO 16750-3 § 4.1.2.2.2.2 | | | | | |
| LEG 6 : Random vibration test | ISO 16750-3 § 4.1.2.2.2.3 | | | | | |
| LEG 7 : Mechanical shocks test | ISO 16750-3 § 4.2 | | | | | |
| SAFETY : Mechanical tests | | | | | | |
| LEG 8 : Free Fall | IEC 60068-2-31 § 5.2 Method 1 | | | | | |
| SAFETY : Insulation tests | | | | | | |
| LEG 1 : Isolation Resistance Test | IEC 60664-1 | | | | | |
| LEG 1 : Dielectric Withstand Voltage | IEC 61010-1 § 6.8.3 | | | | | |
| EMC TESTS | | | | | | |
| Electrostatic discharge immunity test | IEC 61000-4-2 | | | | | |
| Immunity to conducted disturbances | IEC 61000-4-6 | | | | | |
| Radiated electromagnetic field immunity test | IEC 61000-4-3 | | | | | |
| Electrical fast transient/burst immunity test | IEC 61000-4-4 | | | | | |
| FINAL CHARACTERIZATION | | | | | | |
| Linearity error at 25 °C | LEM CO.60.09.014.0 | | | | | |
| Characterization in temperature range | LEM CO.60.09.014.0 | | | | | |
| ANNEX | | | | | | |
| END OF REPORT | | | | | | |
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